

								
	Gate	CV	OUT 1	OUT 2	SYNC	P	A	B
PWM #1	Gate	Pitch	Main out	-1 oct	LFO reset	Pitch	PW	PWM
PWM #2	Gate	Pitch	Main out	-1 oct	LFO reset	Pitch	PW	PWM
PWM #3	Gate	Pitch	Main out	-1 oct	LFO reset	Pitch	LFO frequency	PWM
Chiptune	Gate	Pitch	Main out	-1 oct	Arp reset	Pitch	Arp speed	ARP overtones
Cluster	Gate	Pitch	Main out	-1 oct	Arp reset	Pitch	Cluster overtones*	Cluster frequency
Ramp mod	Gate	Pitch	Main out	-1 oct	LFO reset	Pitch	LFO frequency	LFO range
FM	Gate	Pitch	Main out	-1 oct	LFO reset	Pitch	LFO frequency	Modulation amount
Noise 1	Gate	Pitch	Main out	-1 oct	■ FX amt ■ S&H rate	Pitch	FM jitter S&H rate	PWM jitter S&H rate
Noise 2	Gate	Pitch	Out 1	Out 2	—	Pitch	Density 1	Density 2
Switcher	Clock In	Switch CV	Out 1	Out 2	Offset**	Divisions	Randomness	Hysteresis
Trig Clust	Gate	Frequency	Cluster Out	Gate Out	Oneshot	Speed	log/lin*	Growth
Trig Cloud	Gate	Density	Cluster out	Gate Out	LFO reset	LFO frequency	Density	Random/LFO

(*) Bipolar action.

(**) Adds random offset when no CV-in is used.